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Question 2 requires you to match logic gate symbols to their name by drawing a straight line to link them. Answers.

Example Question 2. Reveal Answers.

10 (a) Question 10 (a) requires you to fill in the truth table for a NOR gate.

Remember that a NOR gate is essentially the opposite of an or gate. X will only be 1 if Neither of the inputs are 1 ...

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design combinational logic circuits □
Combinational logic circuits do not
have an internal stored state, i.e., they
have no memory. Consequently the
output is solely a function of the
current inputs. □ Later, we will study
circuits having a stored internal state,
i.e., sequential logic circuits.

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